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INFORMATION DISCLOSURE CNATION Lam. Yat-Tung et al.							
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	`	PAGE 1 OF 1	January 18, 2001 Assigned 2194			ed 2124	
U.S. PATENT DOCUMENTS							
E.I.		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS	FILING DATE
CO	AA	6035320	3/7/00	Kiriaki et al.		115	CEIVE
CD	AB	5777910	7/7/98	Lu		- MA	Y 0 8 2002
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ÇD	AD	WO 46867	9/16/99	PCT			
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CD	ΑE	"An Adaptive Multiple Echo Canceller for Slowly Time Varying Echo Paths", Yip and Etter, IEEE Transactions on Communications, October 1990					
Cφ	AF	"Digital Signal Processing", Alan V. Oppenheim et al., pp. 155-163					
(D)	AG	"A 100 MHz Output Rate Analog-to-Digital Interface for PRML Magnetic-Disk Read Channels in 1.2 um CMOS", Gregory T. Uehara and Paul R. Gray, ISSCC94/Session 17/Disk-Drive Electronics/Paper FA 17.3, 1994 IEEE International Solid-State Circuits Conference, pp 280-281					
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CD	AJ	"A 50 MHz 70 mW 8-Tap Adaptive Equalizer/Viterbi Sequence Detector in 1.2 um CMOS", Gregory T. Uehara et al., 1994 IEEE Custom Integrated Circuits Conference, pp. 51-54.					
EXAMINER DATE CONSIDERED 5/0/02							

EXAMINER-Initial Creference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.